

### IN THE CLAIMS

Claims 1-31 were originally presented in the application. Claims 1-6 and 19-31 were cancelled in a preliminary amendment. Claims 7-18 are currently pending in the application. Claims 7 and 15 are independent claims and claims 8-14 and 16-18 are dependent claims, depending from claims 7 and 15, respectively. Claims 7 and 13-15 have been amended to clarify the claim language by correcting noticed minor typographical errors. The Applicants assert that the scope of the amended claims has not been modified. Applicants respectfully request reconsideration of pending claims as recited below.

Please amend the claims as follows.

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1-6. (Cancelled)

7. (Currently Amended) A motion estimation processor comprising a controller and a plurality of resolution processors, connected to said controller,  
said plurality of resolution processors analyzing the development of a video signal in time, thereby producing motion analysis,  
said controller controlling said plurality of resolution processors.

8. (Original) The motion estimation processor according to claim 7 wherein said plurality of resolution processors include at least one low resolution processor, for producing low resolution motion analysis.

9. (Original) The motion estimation processor according to claim 7 wherein said plurality of resolution processors include at least one full resolution processor for producing full resolution motion analysis.

10. (Original) The motion estimation processor according to claim 7 wherein said plurality of resolution processors include at least one hyper resolution processor for producing hyper resolution motion analysis.

11. (Original) The motion estimation processor according to claim 8 wherein said at least one low resolution processor reduces the resolution of a selected frame before producing said low motion analysis.

12. (Original) The motion estimation processor according to claim 10 wherein said at least one hyper resolution processor enhances the resolution of a selected frame before producing said hyper resolution motion analysis.

13. (Currently Amended) The motion estimation processor according to claim 11 ~~said~~ wherein said plurality of resolution processors include at least one full resolution processor for producing full resolution motion analysis.

14. (Currently Amended) The motion estimation processor according to claim 9 wherein said plurality of resolution processors include at least one hyper resolution processor, for producing hyper resolution motion analysis.

15. (Currently Amended) A digital signal processor for processing a multiple frame video digital signal, comprising:

a DSP controller,

a plurality of processing units connected to said DSP controller for processing said multiple frame video digital signal; and

at least one storage unit, wherein each of said processing units is connected to at ~~lest~~ least one of said at least one storage units,

said DSP controller controlling said plurality of processing units,

wherein said DSP controller, said plurality of processing units, and said at least one storage unit are on a single chip.

16. (Original) The digital signal processor according to claim 15 wherein each of said processing units is operative to access any storage address of any of said at least one storage unit, connected thereto.

17. (Original) The digital signal processor according to claim 15 wherein each of said processing units operates according to a different program command.

18. (Original) The digital signal processor according to claim 15 wherein each of said processing units operates on a different portion of data.

19-31. (Cancelled)

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